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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)
GILBERT P. HYATT)
Serial No. 06/848,017)
Docket No. 307)
Filed: April 3, 1986)
For: AN INTEGRATED CIRCUIT FILTER)
PROCESSOR)

)

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Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner For Patents
P.O. Box 1450, Alexandria, VA 22313-1450

Transmitted herewith is an Information Disclosure Citation (IDC) listing references for consideration by the Examiner.

The Applicant requests consideration by the Examiner of the references listed on the IDC transmitted herewith and initialing of the references listed on the IDC.

Copies of the listed references are being filed in the PTO contemporaneously herewith.

The listed references are relevant because they were cited by examiners in copending applications of the Applicant.

The instant Information Disclosure Statement is filed in accordance with directions provided by the PTO.¹

1. See the Telephone Conference Record dated February 2, 1999 regarding telephone conversations between SPE Hjerpe and Mr. Turner that took place on December 10, 1998 and February 2, 1999; which Telephone Conference Record was filed with other IDSs of record.

I hereby certify that each of the listed references was cited in one of the actions in the copending applications listed hereinafter, which actions were received no more than three months prior to the mailing of this statement.

<u>DKT.</u>	<u>SERIAL NO.</u>
477	08/460,697
495	08/464,037
553	08/479,423

CERTIFICATION OF MAILING BY EXPRESS MAIL: I hereby certify that this correspondence is being deposited with the United States Postal Service with Express Mail post office to addressee service under 37 CFR 1.10, postage prepaid, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 with the express mail label number EV 323874932 on December 29, 2003.

Respectfully submitted,

Dated: December 29, 2003



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Sheet 1 of 1

Form PTO-1449

INFORMATION DISCLOSURE CITATION
IN AN APPLICATION

Docket Number	Application Number
307	06/848,017
Applicant	Gilbert Hyatt
Filing Date	Group Art Unit
April 3, 1986	

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS
	3,579,201	05/71	Langley	712	245
	3,657,706	04/72	Horgan et al	345	2.1
	3,675,219	07/72	Kawasaki et al	365	73
	4,559,555	12/85	Schoolman		
	4,669,812	06/87	Hoebing	359	9
	4,674,046	06/87	Ozeki et al	382	131
	4,737,921	04/88	Goldwasser et al	345	421
	5,093,567	03/92	Staveley	250	221
	5,130,794	07/92	Ritchey	348	39
	5,135,397	08/92	Yen	434	2
	5,255,211	10/93	Redmond	703	6

OTHER DOCUMENTS *(including Author, Title, Date, Pertinent Pages, Etc.)*

Ferrite Memory systems by Russel, Loulis A. et al., dated IEEE Transactions on Magnetics vol. mag-4, no. 2 June 1968, pp. 134-145.

Current Status of Large Scale Integration Technology by Petritz, Richard L., IEEE Journal of Solid State Circuits vol.sc-2, no. 4 December 1967.

Designing a System on a Chip by Spitalny, Arnold , annual ACM IEEE Design Automation Conference Proceeding of the June 1970 design automation workshop of Design automation, pp. 336-345

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.